

Attorney Docket No. 5308-286

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Sumakeris et al.  
Serial No.: 10/686,795  
Filed: October 16, 2003  
For: METHODS OF FORMING POWER SEMICONDUCTOR DEVICES USING  
BOULE-GROWN SILICON CARBIDE DRIFT LAYERS AND POWER  
SEMICONDUCTOR DEVICES FORMED THEREBY

Date: November 10, 2003

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

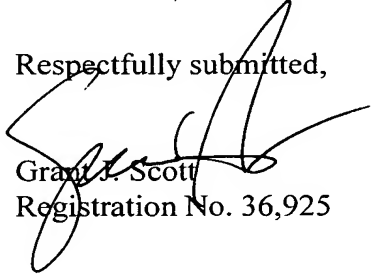
**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Sir:

Attached is a form PTO-1449, together with a copy of each of the identified document(s). It is requested that these documents be considered by the Examiner and officially made of record in accordance with the provisions of 37 C.F.R. § 1.56 and Section 609 of the MPEP.

This Information Disclosure Statement is submitted in accordance with 37 C.F.R. § 1.97(b), within three months of the filing date of the above-referenced application or before the mailing of a first Office Action on the merits, whichever event occurs last. Therefore, no fee is believed due. However, the Commissioner is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

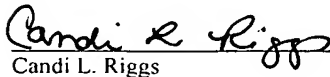
Respectfully submitted,

  
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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on November 10, 2003.

  
Candi L. Riggs

